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L1	24	switch near matrix with equaliz$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 18:55
L2	0	switch$3 near matrix with equaliz$6 with jitter with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 14:44
L3	3	switch$3 near matrix with equaliz$6 and jitter and loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 14:44
L6	11	switch near matrix with equaliz$6 and induc$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:31
L7	0	equaliz$6 with inductance with ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:31
L8	0	equaliz$6 with inductor with ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:34
L9	0	equaliz$6 same inductor same ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:32
L10	1	equaliz$6 and inductor and ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:32

L11	2	equaliz$6 and induct$4 and ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:33
L12	3	switch$4 and induct$4 and ressistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:34
L13	77698	switch$4 and induct$4 and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:34
L14	130	equaliz$6 with inductor with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:35
L15	57	equaliz$6 with inductor with resistor and switch$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 17:25
L16	10	equaliz$6 with inductor with resistor with switch$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 16:35
L17	1	equaliz$6 with inductor with resistor and switch$4 and skin	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 17:25
L19	1425	375/229	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 18:57

L20	558	switch near matrix and equaliz$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 18:55
L21	9	19 and 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 18:58
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L23	3	20 and 22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/09 18:58
S1	1	"10/066019"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/24 07:35
S2	8	"6236524".pn. "6137832".pn. "6005892".pn. "4727578".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/24 07:41
S3	15	switch adj matrix with equaliz$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 07:47
S4	0	input adj ports and output adj ports and eqializ$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 07:48

S5	3211	input adj ports and output adj ports and equaliz$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 07:48
S6	162	input adj ports with output adj ports with equaliz$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 07:49
S7	13	input adj ports with output adj ports with equaliz$5 with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 07:52
S8	0	input adj ports with output adj ports with equaliz$5 with loss and swith with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 07:53
S9	0	input adj ports with output adj ports with equaliz$5 with loss and switch with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 07:53
S10	5	input adj ports same output adj ports same equaliz$5 same loss and switch with matrix	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 19:50
S11	47	switch with matrix and equaliz$5 with loss	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 21:08
S12	59511	hasegawa.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 21:08

S13	2	"5191431".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 21:18
S14	331	asymmetrical with compression	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 21:19
S15	45	asymmetrical adj compression	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 21:43
S16	194	access with request with memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 21:46
S17	70	access adj request with memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 21:46
S18	43	access adj request near memory with throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/04 21:48
S19	4	access adj request near memory near throughput	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 08:36
S20	3	"10091503"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 08:36

S21	1	"10/091503"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/05 08:36

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Preisach, Helmut / ALCATEL, *EUROPEAN PATENT APPLICATION*, Aug 2004
...regenerator with an **equalizer** and a clock...asynchronous space **switching matrix** operable...**switching matrix** contains...input of a **switching** module. Each...contains an **equalizer** and a clock...7 shows a **switching matrix** used in the...contains an **equalizer** 2, a clock...
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BATRUNI, Roy, G. / OPTICHRON, INC., *PATENT COOPERATION TREATY APPLICATION*, Nov 2004
...network is a minimum-maximum **switching matrix** that connects a maximum processor...topology of the minimum-maximum **switching matrix** connecting the I feedforward...702). 1 the minimum- maximum **switching matrix**, the minimum operations in the...
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...cost for the thick client STB. The FSN used ATM both for its **switching** and transport protocol needs. ATM is very inefficient for...to be segmented into ATM cells for delivery through the ATM **switching** network. At the STB, AAL-5 was used to reassemble the MPEG...
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      Mar 2005
      ...Sony HDCAM video format. ECL (Emitter Coupled Logic) A variety of bipolar transistor
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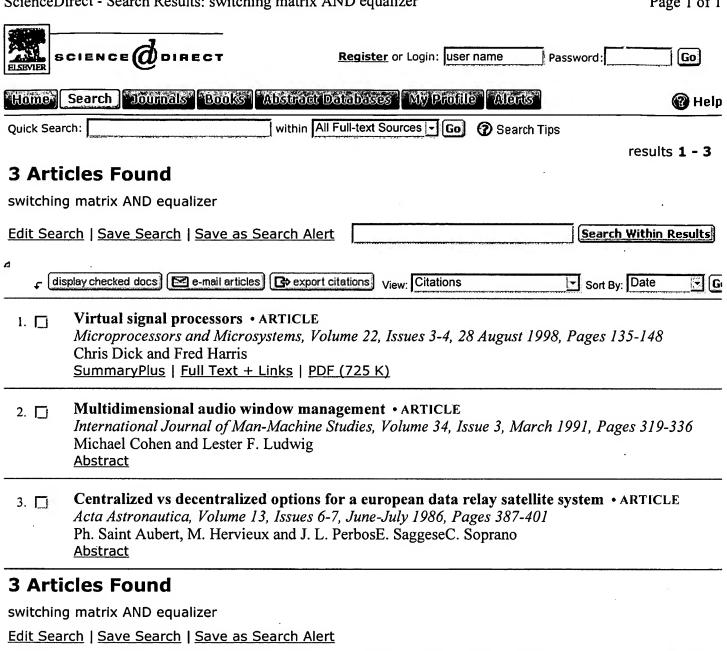   [http://edocs.tu-berlin.de/diss/2003/maier_martin.pdf]
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